राष्ट्रीय प्रौद्योगिकी संस्थान दिल्ली
NATIONAL INSTITUTE OF TECHNOLOGY DELHI
(शिक्षा मंत्रालय, भारत सरकार के अधीन एक स्वायत्त संस्थान)

(An autonomous Institute under the aegis of Ministry of Education (Shiksha Mantralaya), Govt. of India)
Plot No. FA7, Zone P1, GT Karnal Road, Delhi-110036, INDIA
दूरभाष/Tele: +9111-33861000, 1001, 1005 फैक्स/ Fax: +9111-27787503,
वेबसाइट/Website: www.nitdelhi.ac.in

Date: 29.11.2023

ADVERTISEMENT FOR JUNIOR RESEARCH FELLOW (JRF) under the
Chips to Startup (C2S) Programme

Applications in the prescribed formats are invited from highly motivated and dynamic eligible candidates for one position of **Junior Research Fellow (JRF)** in the following Ministry of Electronics & Information Technology (MeitY), R&D in Electronics Group (Microelectronics Development Division), Govt. of India sponsored research project:

Project Title: A Novel power-on pilot IC for ultra-low power wireless IoT devices

Funding Agency: Ministry of Electronics & Information Technology (MeitY)

Project Duration: Three (03) years or position is coterminous with the project.

Chief Investigator & Co-Chief Investigator (CI & Co-CI):

Dr. Baljit Kaur & Dr. D. Vaithiyanathan,

Department of Electronics and Communication Engineering, National Institute of Technology Delhi

The minimum essential and desirable qualifications for the project posts are as follows:

Name of the Position	Junior Research Fellow (JRF), One (01)
Essential Qualifications	• Candidates with Bachelor's degree in Engineering/Technology in Electronics and Communication Engineering or Electrical and Electronics Engineering or Computer Science and Engineering or other relevant discipline. and • Master's degree in Engineering/Technology in the fields of VLSI Design/ Microelectronics/Embedded Design/ECE or other relevant discipline, with a minimum of 60% aggregate marks (or) CGPA of 6.5/10 in UG or PG for JRF under GEN/EWS/OBC category and 55% aggregate marks or equivalent CGPA of 6.0 for SC/ST candidates.
Duration	03 Years The position is purely on contract basis with the initial appointment for a period of one (01) year which can be extended up to the completion of the project, purely based on the satisfactory performance. The position is co-terminus with the project.
Emoluments	Rs. 31,000/- (for the first 2 years) and Rs. 35,000/- pm (for the 3rd year) plus House Rent Allowance (HRA) as per the norms of GOI

Desirable Qualification/ Skills:	Knowledge in Verilog HDL, Cadence Tool, Siemens EDA Tools (Mentor graphics), Synopsys TCAD etc.
Nature of appointment	It will be governed by the administrative rules conditions of Research & Consultancy NIT Delhi / Institute norms.
Other Benefits	**The selected candidate shall be encouraged to register for Ph.D. at NIT Delhi as per institute norms.**
Age Limit	**Not exceeding 30 years (for JRF) as on the closing date of the application.** **Age relaxation as per GOI norms.**

HOW TO APPLY:

1. Eligible and Interested candidates should send their duly filled application along with the following:

   (i) Completely filled in application form in the attached format (Annexure-I)

   (ii) Detailed curriculum vitae CV with particulars of prior research experience, publications, if any.

   (iii) Self-attested copies of the relevant certificates/documents

   A single .pdf file with the subject line: "Application for JRF Position under C2S Programme" be addressed to Dr. D.Vaithiyanathan, Co-Chief Investigator of the project, through an email: dvaithiyanathan@nitdelhi.ac.in on or **before 20th December, 2023**.

   PLEASE DONOT SEND ANY HARD COPY

INSTRUCTIONS:

1. Applications, which are not in the prescribed format and partially filled, shall be summarily rejected. No correspondence shall be entertained in this regard.

2. **The last date of receiving applications is 20-December-2023.**

3. No TA/DA is permissible for appearing in the interview and joining in case of selection.

4. The selected candidate shall be encouraged to apply and register for Ph.D. Programme of the Institute as per the prevailing norms. Preference may be given to candidates willing to pursue Ph.D. degree at NIT Delhi.

5. Appointment will be purely project based temporary position as per the MeitY norms.

6. The date of interview (tentatively 28.12.2023) along with the shortlisted candidates called for interview will be displayed on the Institute's website or informed through email by Co-CI.

7. Candidate shall bring along with them the original degree(s)/certificate(s) and experience certificate(s) at the time of interview for verification.

8. Candidates will be informed by e-mail about the interview date. So, candidate must provide valid e-mail IDs in their Applications.

9. The applicant will be responsible for the authenticity of information, other documents and photographs submitted.

10. Selected candidate will have to join assignment immediately on the receipt of the offer letter.

11. Appointed candidate will be placed at the National Institute of Technology Delhi and appointment will be made purely on temporary basis initially for a period of one year and can be extended up to the completion of the project based on the performance. The contract will also include a one-month notice period wherein if the candidate selected wants to discontinue the project, he/she will have to inform the CI/Co-CI one month in advance and in case CI/Co-CI is not satisfied with the performance of the candidate, he will need to inform the candidate one month prior to termination of contract.

12. Any queries related to the above post must be addressed to: Dr. D.Vaithiyanathan, Email: dvaithiyanathan@nitdelhi.ac.in.

Dr. D.Vaithiyanathan
Assistant Professor
Department of Electronics and Communication Engineering
National Institute of Technology Delhi
Plot No. FA7, Zone, P1, GT Karnal Rd, Delhi, 110036
Email: dvaithiyanthan@nitdelhi.ac.in
Phone No.: 011-1133861154/ +91-9865806300

Copy to:

1. PS to Director (for kind information).

2. Dean (R&C).

3. Head of Department, ECE.

4. Office of Registrar.

5. Computer Centre (for displaying the advertisement on the Institute website).

6. Office Copy/Project File.

7. Institute Notice Boards.

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Annexure-1

Application Form for Position of JRF under C2S Programme

(MeitY/3080449/03.05.2023)

Application for the post of ....

A. Personal Details:

			Attach a Passport size Photograph
1.	**Name (in block letters):**		
2.	**Father's Name:**		
3.	**Date of Birth:**		
4.	**Gender:**		
5.	**Email Address:**		
6.	**Marital Status (Single /Married)**		
7.	**Contact Number (s):**		
8.	**Address for correspondence:**		
9.	**Permanent Address:**		
10.	**Category (Gen./OBC/SC/ST):**		
11.	**Aadhar Number**		

B. Educational Details:

Degree	Subjects	University/Institution	Year of passing	Grade or % of marks
10th				
12th				
BE/BTech				
ME/MTech				
Other				

C. Experience Details (Teaching/ Industry/ Research)

(In reverse Chronological order i.e. starting with latest experience first)

S. No.	Organization	Designation	Nature of Work	Duration	Salary

D. Details of GATE/NET Qualification

Qualification Year	
Discipline	
Score	
Rank	
Marks Obtained	
Validity	

E. Other Details

Details of Professional Skills	
Title of project dissertation	
How the above position is helpful in meeting your career goal (Max 250 words)	Attach a separate sheet
Details of Research Publications (If any)	Attach a separate sheet
Any other relevant information	
References (Two) (Name of two referees who are familiar with your academic and research background)	**1.** _____ **2.** _____

DECLARATION

I hereby declare that the above information is correct to the best of my knowledge and belief. I understand that my candidature will   be cancelled if any information is found incorrect at any given period of time.

(Signature of the applicant)

Date: **Name of the Candidate:**

Place:

Note:

1. Original Certificate with one copy to be brought at time of Interview only.