



**SOCIETY FOR ELECTRONIC TRANSACTIONS AND SECURITY [SETS]**  
MGR Knowledge City, CIT Campus, Taramani, Chennai – 600 113, India.

**Advertisement No. SETS/Chn/Rec/Proj/2024-25/27 Date: 31<sup>st</sup> May 2024**

**Society for Electronic Transactions and Security [SETS]** is a Society under Societies Registration Act, XXI of 1860, dedicated to carry-out Research and Development in the field of Information Security focusing on the key verticals, namely, Cryptology and Computing, Hardware Security, Quantum Security and Network Security.

SETS invites applications from citizens of India for filling up the following positions for a Research & Development project in the area of Cybersecurity:

- 1. Project Associate – 4 Positions (Software)**
- 2. Project Associate – 2 Positions (Embedded System)**

The descriptions of positions, detailed qualification requirements and salary are given below:

**1. Project Associate (Software): Post code: 24-CCRG-PA-02**

<b>Name of the Post</b>	<b>Project Associate (Software)</b>
<b>Number of Post(s)</b>	Four
<b>Age Limit</b>	Not more than 35 years as on 14.06.2024
<b>Qualification</b>	First Class in B.E / B.Tech in ECE/CSE/IT
<b>Desirable Qualification</b>	First Class in M.Tech /M.E / MS by Research (Computer Science/ Information Technology/Information Security/Cyber security)
<b>Areas of skill sets/ Knowledge required</b>	a) Experience on programming and scripting languages: C, C++, Java, JavaScript, Python, Rust b) Basic Knowledge on Django/Flask Frameworks c) Knowledge on PyQt framework. d) Basic knowledge on Networking protocols: TCP, IP, UDP, SSL/TLS e) Knowledge on Linux operating system (Debian/Ubuntu) f) Basic knowledge on Cryptography, PKI ecosystem - CA, OCSP, CRL
<b>Experience</b>	0- 4 Years of Relevant Experience Equivalent work experience in lieu of higher degree: - Engineering Masters: Two years
<b>Remuneration</b>	Consolidated salary of Rs. 30,000 – Rs 50,000/- per month, additional increment may be given based on the skill sets and relevant experience of candidate. (based on performance of the first year 5-8% increment may be considered)

## 2. Project Associate (Embedded System) – 2 Positions Post code: 24-CCRG-PA-03

<b>Name of the Post</b>	<b>Project Associate (Embedded System)</b>
<b>Number of Post(s)</b>	Two
<b>Age Limit</b>	Not more than 35 years as on 14.06.2024
<b>Qualification</b>	First Class in B.E / B.Tech in ECE/EEE/EIE
<b>Desirable Qualification</b>	First Class in M.Tech /M.E/ MS by Research (Embedded Systems / Communication Systems / Electronic Engineering / Computer Science/Cyber security)
<b>Areas of skill sets/ Knowledge required</b>	a) Programming Experience in C, C++, embedded C, Python, Rust b) Good knowledge on serial interfaces like SPI, I2C, UART and knowledge on USB interfaces like HID, CCID etc. c) Knowledge of PCB layout design and fabrication. d) Knowledge on micro-controller boards like Arduino Due, ESP32, ESP8266, STM32, RP2040 PICO, nRF52840 etc. e) Knowledge on SDKs/IDEs like ESP-IDF, PICO-SDK, OpenSK, Arduino etc. f) Basic knowledge on Cryptography
<b>Experience</b>	0-4 Years of Relevant Experience  Equivalent work experience in lieu of higher degree: - Engineering Masters: Two years
<b>Remuneration</b>	Consolidated salary of Rs. 30,000 – Rs 50,000/- per month, additional increment may be given based on the skill sets and relevant experience of candidate. (based on performance of the first year 5-8% increment may be considered)

The positions of Project Associates as proposed is purely temporary and would be filled on a contract basis with a consolidated salary under project mode. The duration of the assignment is initially for a period of One Year and would be extended further based on the need of the project and performance. There is no scope of continuation/regularization/absorption under any circumstances.

### Application Procedure:

- Only applications received via Google Form will be considered. The Candidate should choose the appropriate post to apply (“Project Associate - (Software)/ Project Associate (Embedded System)”). Please find below the link for the Google form:**

<b>Name of the Post</b>	<b>Post code</b>	<b>Google Form Link</b>
<b>Project Associate (Software)</b>	<b>24-CCRG-PA-02</b>	<a href="https://forms.gle/WBTuxLjVrVUHs3Tq5">https://forms.gle/WBTuxLjVrVUHs3Tq5</a>
<b>Project Associate (Embedded System)</b>	<b>24-CCRG-PA-03</b>	<a href="https://forms.gle/BHxwremPSQRRW9FD9">https://forms.gle/BHxwremPSQRRW9FD9</a>

2. The candidate is requested to attach the following documents:
  - a. Scanned copy of Personal Particulars form (pdf format) duly filled and signed. The Personal Particulars form is available at SETS homepage <https://setsindia.in/careers>.
  - b. Scanned copies of academic certificates (Single PDF document)
  - c. Scanned copies of Experience certificates (Single PDF document)
3. The last date for uploading applications via Google Form is 14.06.2024.
4. Shortlisted candidates would be required to attend a Written Test and/or Interview at SETS, Chennai. The date and time for this would be intimated to shortlisted candidates by E-mail.

**Terms and Conditions:**

1. The shortlisted candidates would be required to bring all their original testimonials for verification on the interview day.
2. No TA/DA will be given to candidates appearing for interview.
3. The prescribed qualifications are minimum and mere possession of the same does not entitle the candidate to be called for the written test or interview. The decision of the Executive Director of SETS in all matters relating to eligibility, acceptance or rejection of the applications, cancellation of advertisement and filling up or not filling up of post will be final and no inquiry or correspondence will be entertained in this matter.
4. Number of vacancies may increase/decrease depending upon SETS requirements and such changes will be made by SETS without any notice

**Executive Director**